

## ABSTRACT:

In an integrated circuit comprising a processor (1), a data memory (2) and function blocks (4,5) a first data bus is provided for the communication among them. Previous to a data transmission of large datasets, according to the invention a direct connection is provided between at least two function blocks (4,5) by way of a second data bus (6). One function block (4) then acts as master and one function block (5) as a slave. The slave has a linear address space which the function block (4) acting as master can access via a handshake method. In the function block (5) acting as a slave the linear address space is mapped onto a buffer memory.

10 Fig. 1